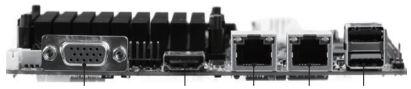
EN-N2807DL Quick Installation Guide-V1.0

Tips:
How to identify the first pin of the jumpers and connectors
1. The first pin is marked as "1"or solder pad or bold lines or triangular symbols
2. The red line on the cable or other marks show that they should be connected with the first pin of the socket.

Warning!
Please adopt appropriate screw and proper installation methods(including board allocation,CPU and heat sink installation,etc);otherwise,the board may damaged.

Back Panel Interface:



Connectors and Jumpers:

10. COM1-4(Front end COM port)

Be used with modems,serial printers,remote display terminals and serial devices.

Pin#	Definition	Pin#	Definition
1	-NDCD1	2	NRXD1
3	-NTXD1	4	-NDTR1
5	GND	6	-NDSR1_1
7	-NRTS1	8	-NCTS1
9	-NRI1_1	10	NC
11	-NDCD2	12	NRXD2
13	-NTXD2	14	-NDTR2
15	GND	16	-NDSR2_2
17	-NRTS2	18	-NCTS2
19	-NRI2_2	20	NC
21	-NDCD3	22	NRXD3
23	-NTXD3	24	-NDTR3
25	GND	26	-NDSR3_3
27	-NRTS3	28	-NCTS3
29	-NRI3_3	30	NC
31	-NDCD4	32	NRXD4
33	-NTXD4	34	-NDTR4
35	GND	36	-NDSR4_4
37	-NRTS4	38	-NCTS4
39	-NRI4_4	40	NC

9. LPT(LPT Header)

Pin#	Definition	Pin#	Definition
1	P_STB	2	P_AFD
3	P_D0	4	ERRJ
5	P_D1	6	P_INIT
7	P_D2	8	P_SLIN
9	P_D3	10	GND
11	P_D4	12	GND
13	P_D5	14	GND
15	P_D6	16	GND
17	P_D7	18	GND
19	-ACK	20	GND
21	BUSY	22	GND
23	PE	24	NC
25	SLCT		

8. CLR_CMOS

If you encounter the following,
a) CMOS data becomes corrupted.
b) You forgot the supervisor or user password.
You can reconfigure the system with the default values stored in the ROM BIOS.
To load the default values stored in the ROM BIOS, please follow the steps below.
1. Power-off the system and unplug the power cord.
2. Short Pin1 and Pin2 for 3-5 seconds, then back to default setting.
3. Plug the power cord and power on the system.

Pin#	Definition
1	GND
2	CLR_CMOS

7. F_AUDIO(Front Panel Audio Header)

Be used connect to the second line-out and MIC in jacks that are at the front panel of you system.

Pin#	Definition	Pin#	Definition
1	LINE_R_CN	2	MIC_R_CN
3	GND	4	GND
5	GND	6	GND
7	LINE_L_CN	8	MIC_L_CN

6. M-PCIE,M-sata(Mini-PCIe expended slots)

Mini-PCIe1 support SATA2,for example,can use SATA SSD etc.
Mini-PCIe2 support PCIe+USB+3G signal,can use MiniPCIe WiFi/BT/3G moudle etc.

5. GPIO_CON

Pin#	Definition	Pin#	Definition
1	GPIO35	2	GPIO44
3	GPIO68	4	GPIO56
5	GPIO1	6	GPIO34
7	GPIO7	8	GPIO49
9	+3.3V	10	GND

4. F_USB(Frond-end USB2.0 Pin)

Be used extension the front USB ports.

Pin#	Definition	Pin#	Definition
1	+5V	2	+5V
3	USB2.0_N1	4	USB2.0_N2
5	USB2.0_P1	6	USB2.0_P2
7	GND	8	GND
9	N/A	10	GND

3. VGA_OUT (VGA Header)

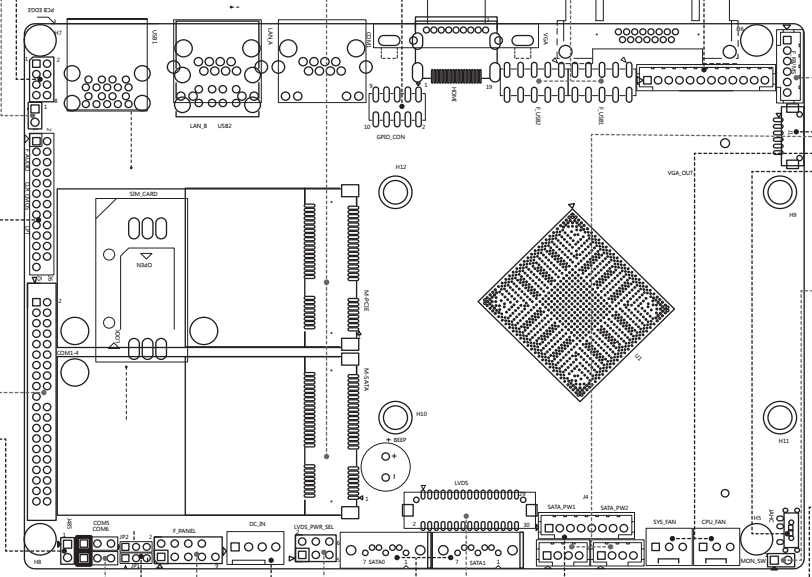
Output the Analog Signal

Pin#	Definition	Pin#	Definition
1	NC	7	VGA_G
2	VSYNC	8	GND
3	HSYNC	9	VGA_B
4	GND	10	GND
5	VGA_R	11	DATA
6	GND	12	CLK

2. F_KB/MS

PS2 Header

Pin#	Definition
1	POWER
2	KDAT
3	KCLK
4	MDAT
5	MCLK
6	GND



1. J1 (IR connector)

Pin#	Definition	Pin#	Definition
1	PS_ON_R	2	IR_TX
3	GND	4	USB_MCUP
5	USB_MCUN	6	IR_POWER
7	GND	8	GND

23. JAHC ON/OFF

Pin#	Definition
1~2	ON
2~3	OFF

22. MON_SW(Panel Switch)

LVDS Panel Switch: ON(Default)

Pin#	Definition
1	DISPLAY_ON/OFF
2	GND

21. CPU_FAN/SYS_FAN

Be used for connecting CPU fan

Pin#	Definition
1	GND
2	+12V
3	FAN_TAC

Be used for connecting chassis fan

Pin#	Definition
1	GND
2	+12V
3	FAN_TAC2

20. SATA_PW

SATA Power supply

Pin#	Definition
1	+5V
2	GND
3	GND
4	+12V

11. J485(COM6 RS485 Header)

Pin#	Definition
1	S485+
2	S485-

12. COM5.COM6(RS232 Header)

Pin#	Definition
1	RXD
2	TXD
3	GND

13. JP1/JP2(COM6 RS232/RS485 Mode Jumper)

Jumper	Pin#	Definition
JP1	1-2	RS485
JP2	1-2	
JP1	2-3	RS232
JP2	2-3	

14. F_PANEL(Front-end control panel)

Be used connect the power switch、reset switch、chassis intrusion switch/sensor and system status indicator on the chassis.
1~3 HDD-LED
2~4 PWR-LED
5~7 RESET-SWITCH
6~8 PWR-SWITCH

Pin#	Definition	Pin#	Definition
1	+HDLED	2	PLED+
3	GND	4	GND
5	RESET	6	PSW
7	GND	8	GND

15. DC_IN 12V (12V Power receptacle)

This board has special 12V power receptacle for CPU. For better and more stable processor power supply, we suggest keeping the connection on this socket. The definitions of the pins are described below :

Pin#	Definition
1	12V_STBY
2	12V_STBY
3	GND
4	GND

16. LVDS_PW

LVDS Power Select jumper

Pin#	Definition
1~2	12V
3~4	5V
5~6	3.3V

17. SATA0-1(Serial ATA flat-cable sockets)

Black color socket is SATAII.

Pin#	Definition	Pin#	Definition
1	GND	6	SATA_RXP0
2	GND	7	SATA_RXN0
3	SATA_TXP0	8	GND
4	SATA_TXN0	9	GND
5	GND		

18. LVDS(Dual-channel 24 bit LVDS Header)

Pin#	Definition	Pin#	Definition
1	LVDS_PWR	2	LVDS_PWR
3	LVDS_TX_CLKAP	4	LVDS_TX_CLKBP
5	LVDS_TX_CLKAN	6	LVDS_TX_CLKBN
7	GND	8	GND
9	LVDS_TX_A0P	10	LVDS_TX_B0P
11	LVDS_TX_A0N	12	LVDS_TX_B0N
13	GND	14	GND
15	LVDS_TX_A1P	16	LVDS_TX_B1P
17	LVDS_TX_A1N	18	LVDS_TX_B1N
19	GND	20	GND
21	LVDS_TX_A2P	22	LVDS_TX_B2P
23	LVDS_TX_A2N	24	LVDS_TX_B2N
25	GND	26	GND
27	LVDS_TX_A3P	28	LVDS_TX_B3P
29	LVDS_TX_A3N	30	LVDS_TX_B3N

19. LVDS_CL

LVDS MENU Adjustment control interface

Pin#	Definition
1	BLT_DOWN
2	BLT_UP
3	GND
4	GND
5	VIN_LVDS_F
6	VIN_LVDS_F
7	BKLT_PWM
8	BKLT_EN